

PATENT
03901-P0004A ELF

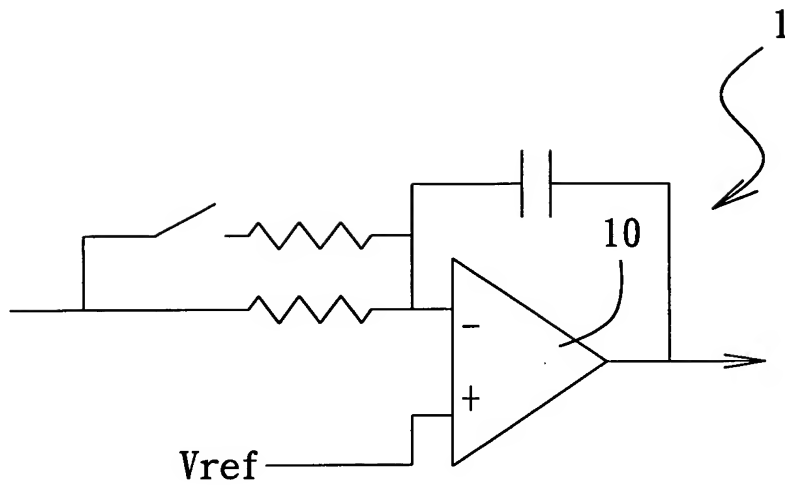
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	Tsin-Yuan Chang, et al.
Serial No. - Pending	December 30, 2003
Title of Application:	Method And Apparatus For Controlling A Dual-Slope Integrator Circuit To Eliminate Settling Time Effect

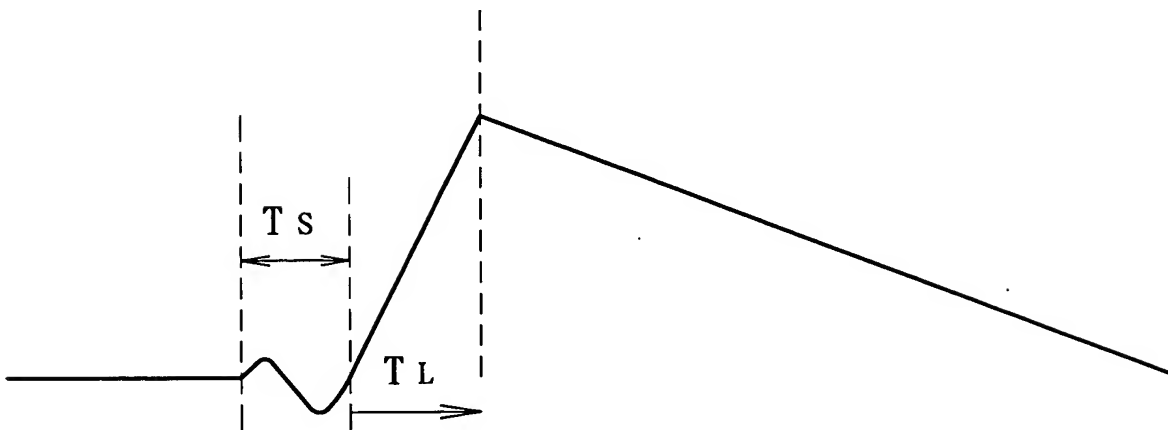
Mail Stop Patent Application
Commissioner for Patents
Post Office Box 1450
Alexandria, VA 22313-1450

Cover Sheet For Four Sheets Of Drawings

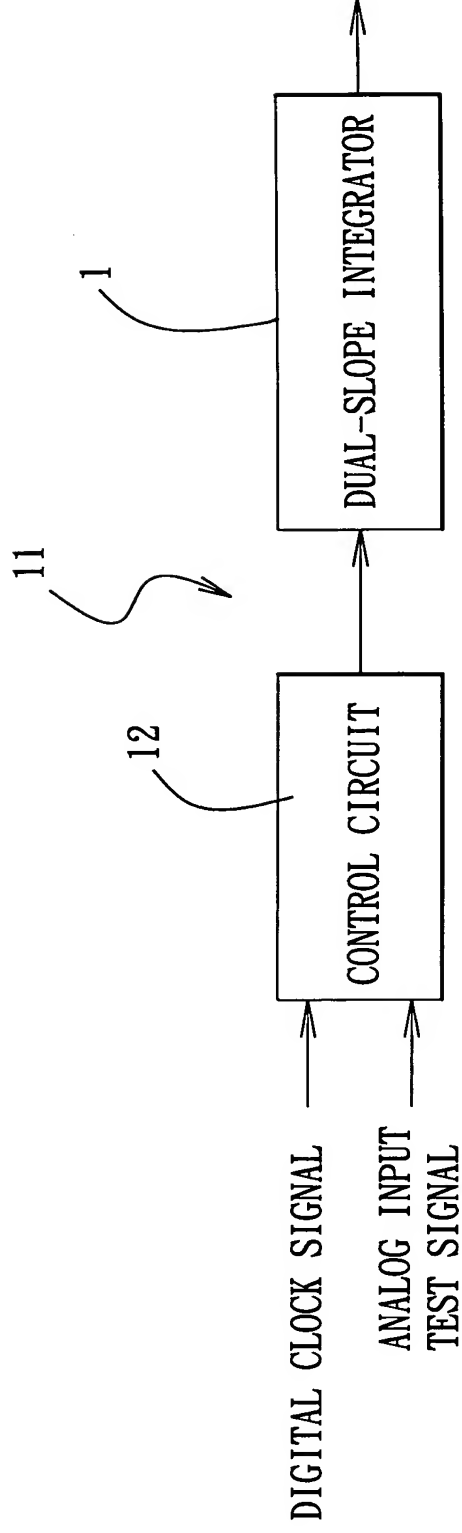
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F I G. 1
PRIOR ART



F I G. 2
PRIOR ART



F I G. 3
PRIOR ART

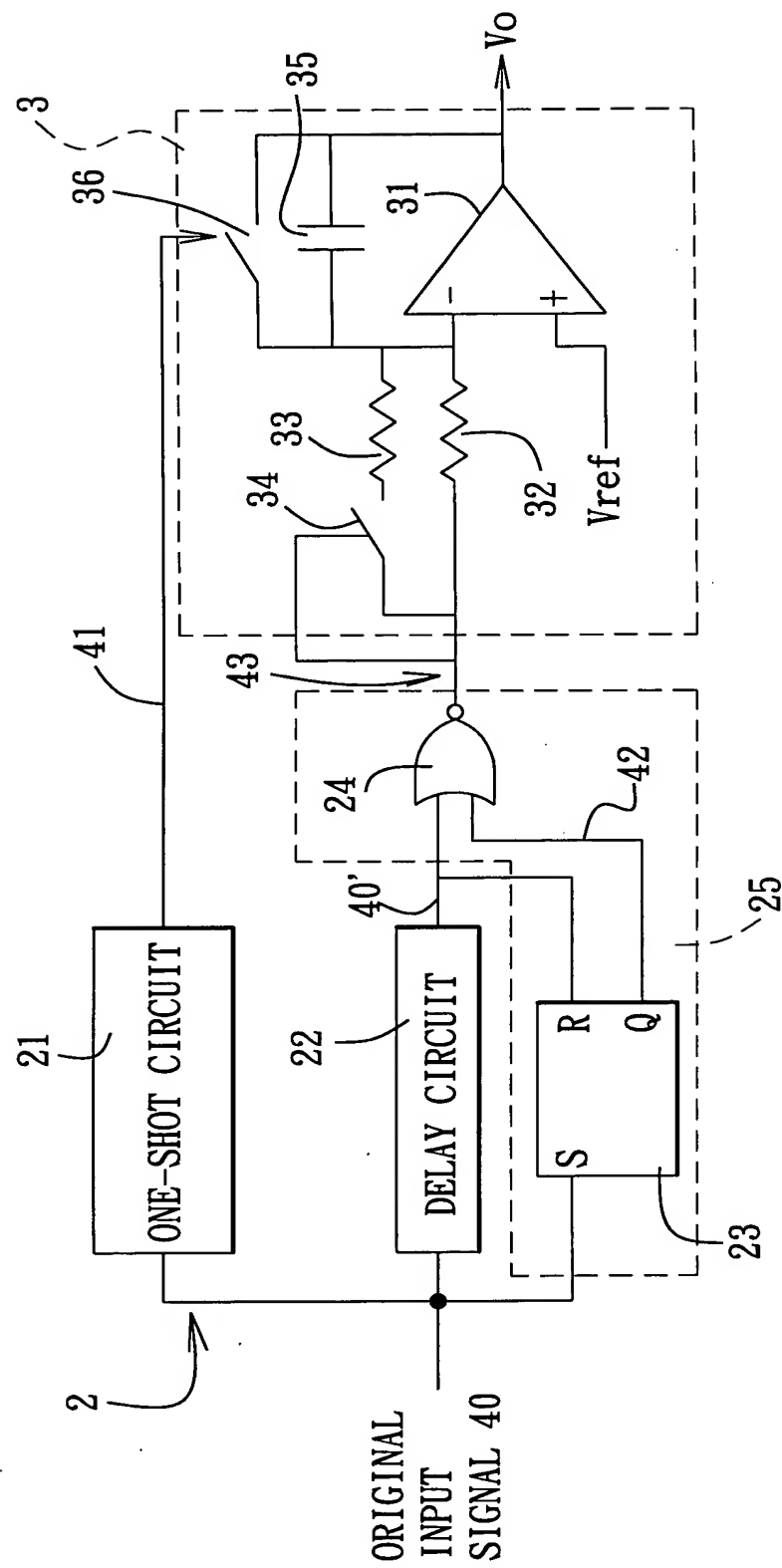


FIG. 4

FIG. 5 A

ORIGINAL INPUT
SIGNAL 40

FIG. 5 B

DELAYED INPUT
SIGNAL 40'

FIG. 5 C

RESET SIGNAL 41

FIG. 5 D

TRIGGER SIGNAL 43

FIG. 5 E

INTEGRATOR OUTPUT
SIGNAL (V_o)

FIG. 5 F

LATCH OUTPUT
SIGNAL 42

